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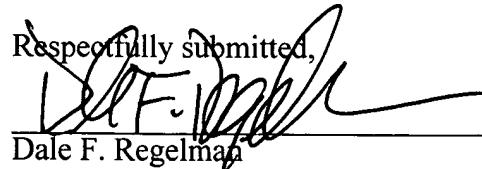
APPLN. OF: BEESTON, et al.  
SERIAL NO: 09/837,133  
FILED: APRIL 18, 2001  
FOR: METHOD TO VALIDATE SYSTEM CONFIGURATION  
GROUP: 2115  
EXAMINER: SURYAWANSHI, SURESH DOCKET: TUC920010028US1

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-140

**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

In response to the Notice of Allowability dated October 6, 2004, submitted herewith are six (6) sheets of formal drawings with respect to the above-identified patent application.

Respectfully submitted,  
  
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